



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

#3/Election
10/10/02
1/8

In re Application of:

Gerald J. D'Amato, et al.

Serial No.: 09/915,076

Filing Date: 07/25/01

Title: METHODS AND APPARATUS
FOR ALIGNING AN INTEGRATED
CIRCUIT PACKAGE WITH AN
INTERFACE

Atty. Dckt. No.: AG10010311-1

Group Art Unit: 2827

Examiner: Thai, Luan C.

To The Honorable Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Dear Sir:

In response to the Election/Restriction Requirement dated September 3, 2002, applicants elect the claims of Group I for examination (i.e., claims 1-18, 26 and 27).

Respectfully submitted,
DAHL & OSTERLOTH, L.L.P.

By:

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